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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10044501	01/10/2002	324	158.1	2829	01/10/2002

\*\*APPLICANTS: Babcock Douglas;

\*\*CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CIP OF 09/757,746 01/10/2001  
AND CLAIMS BENEFIT OF 60/265,729 02/01/2001  
AND CLAIMS BENEFIT OF 60/267,162 02/07/2001

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		Analog 5167CIP
Verified and Acknowledged Examiners's initials		
TITLE : Apparatus and method for driving circuit pins in a circuit testing system		
U.S. DEPT. OF COMM /PAT & TM-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Total Claims	Print Claim for O.G	
Assistant Examiner		DRAWING		
		Sheets Drwg.	Figs. Drwg.	Print Fig.
Primary Examiner		Application Examiner		
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		
DISCLAIMER				

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